

Electronic Acknowledgement Receipt

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Application Number:	09943512
International Application Number:	
Confirmation Number:	8183
Title of Invention:	Multilayer wiring board and semiconductor device
First Named Inventor/Applicant Name:	Yasuo Osone
Customer Number:	20457
Filer:	Gregory Edwards Montone/David Snyder
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Supplemental Response or Supplemental Amendment	SUPPLAMD.pdf	59496 8a8000a8b5ac0b5cc4824c93b41f1485 e46c	no	4

Warnings:

Information:

2	Drawings-only black and white line drawings	DWG.pdf	20674 <small>bc9cass24bba915f74ad706c1dfe5e3e8f cc30b</small>	no	1
Warnings:					
Information:					
Total Files Size (in bytes):			80170		
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